



9200/2822

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

WILLIAM D. ROCHE
DAVID D. WU
MASSUD AMINPUR
SCOTT D. LUNING

Group Art Unit: 2822

Examiner: K. Duong

Atty. Dkt. No.: 2000.032200/TT3634

Serial No.: 09/676,197

Filed: September 28, 2000

For: METHOD OF FORMING A GATE
ELECTRODE ON A SEMICONDUCTOR
DEVICE AND A DEVICE
INCORPORATING SAME

RECEIVED
MAY 30 2003
TECHNOLOGY CENTER 2800

PETITION TO WITHDRAW HOLDING OF ABANDONMENT
UNDER 37 CFR 1.181 (a)

Mail Stop: Petition
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as "FIRST CLASS MAIL" addressed to: Mail Stop Petition, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, this 20th day of May, 2003.

Kathy Deros

Signature

Sir:

This Petition to Withdraw Holding of Abandonment under 37 CFR 1.181 (a) is submitted in response to the Notice of Abandonment dated May 1, 2003. As this paper is filed within two months of the mailing date of being Notice of Abandonment, it is believed that this Petition is timely filed and that no fee is due. However, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Assistant Commissioner is authorized to deduct said fees from **Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT3634.**


In the Notice of Abandonment, the Examiner held that the above-referenced application was abandoned because Applicants failed to timely file new formal drawings as required in the Notice of Allowability. The Examiner alleges that no proposed new formal drawings have been received. Applicants contend that new formal drawings were timely filed with the issue fee on January 17, 2003 and that the above-referenced application is not in fact abandoned. The following evidence is submitted herein in support of this contention:

- (a) a copy of the Issue Fee Transmittal Form mailed on January 17, 2003;
- (b) a copy of the postcard date stamped by the OIPE on January 22, 2003, indicating receipt of the Issue Fee Transmittal Form;
- (c) a copy of the Submission of Formal Drawings Pursuant to Notice of Allowability mailed on January 17, 2003;
- (d) a copy of the postcard date stamped by the OIPE on January 22, 2003, indicating receipt of the Submission of the Formal Drawings.

The Examiner is invited to contact the undersigned at (713) 934-4052 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

Date: 5/20/03



Mark W. Sincell, Ph.D.
Reg. No. 52,226
WILLIAMS, MORGAN & AMERSON, P.C.
10333 Richmond Avenue, Suite 1100
Houston, Texas 77042
(713) 934-7000
(713) 934-7011 (facsimile)

AGENT FOR APPLICANTS

Please indicate receipt of the below-identified paper:

<input type="checkbox"/> New Application For:	/	Priority Date:
<input type="checkbox"/> Foreign priority already claimed	<input type="checkbox"/> Nonpublication Request	
<input type="checkbox"/> Continuation	<input type="checkbox"/> CIP	
<input type="checkbox"/> Specification:	Pages	
<input type="checkbox"/> Response to Office Action Dated:	Drawings: 10 Sheets	
<input checked="" type="checkbox"/> Other: Issue Fee Transmittal and Patent Postcard	<input type="checkbox"/> Final Rejection	
<input type="checkbox"/> Assignment Enclosed	<input checked="" type="checkbox"/> Cert. of Timely Mailing	<input type="checkbox"/> Exp. Mail:

ORIGINAL

JAN 22 2003
PATENT
RECEIVED

IDENTIFICATION OF APPLICATION

Serial No.:	09/676,197
Title:	METHOD OF FORMING A GATE ELECTRODE ON A SEMICONDUCTOR DEVICE AND A DEVICE INCORPORATING SAME
Applicant:	William R. Roche
Client:	Advanced Micro Devices, Inc.
Mailed:	01/17/03
Filed:	09/28/2000
Attorney:	JMA/MWS
File No.:	2000.032200/TT3634
Due Date:	03/03/2003

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail** Box ISSUE FEE
Commissioner for Patents
Washington, D.C. 20231
Fax (703)746-4000

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 4 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Legibly mark-up with any corrections or use Block 1)

023720 7590 12/03/2002

WILLIAMS, MORGAN & AMERSON, P.C.
 10333 RICHMOND, SUITE 1100
 HOUSTON, TX 77042



Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmittal

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Box Issue Fee address above, or being facsimile transmitted to the USPTO, on the date indicated below.

Kathryn Danas	(Depositor's name)
<i>Kathryn Danas</i>	(Signature)
January 17, 2003	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/676,197	09/28/2000	William R. Roche	2000.032200	9021

TITLE OF INVENTION: METHOD OF FORMING A GATE ELECTRODE ON A SEMICONDUCTOR DEVICE AND A DEVICE INCORPORATING SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1280	\$0	\$1280	03/03/2003

EXAMINER	ART UNIT	CLASS-SUBCLASS
DUONG, KHANH B	2822	438-585000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
- ☐ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

Williams, Morgan &
 1. Amerson
 2.
 3.

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Advanced Micro Devices, Inc.

Austin, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent)

☐ individual ☐ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

☒ Issue Fee

☐ Publication Fee

☒ Advance Order - # of Copies 3

4b. Payment of Fee(s):

☐ A check in the amount of the fee(s) is enclosed.

☐ Payment by credit card. Form PTO-2038 is attached.

☒ The Commissioner is hereby authorized by charge the required fee(s), or credit any overpayment, to Deposit Account Number 01-0365/7369 (lose an extra copy of this form).

Commissioner for Patents is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

(Authorized Signature)

(Date)

Michael J. Roche

1/17/03

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, D.C. 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

TRANSMIT THIS FORM WITH FEE(S)

RECEIVED
 MAY 30 2003
 TECHNOLOGY CENTER 2800

Please indicate receipt of the below-identified paper:

<input type="checkbox"/> New Application For:	Priority Date:
<input type="checkbox"/> Foreign priority already claimed	<input type="checkbox"/> Nonpublication Request
<input type="checkbox"/> Continuation	<input type="checkbox"/> Divisional
<input type="checkbox"/> Specification: Pages	<input checked="" type="checkbox"/> Drawings: Fig 6 (corrected drawings)
<input type="checkbox"/> Response to Office Action Dated:	<input type="checkbox"/> Final Rejection
<input checked="" type="checkbox"/> Other: Submission of Formal Drawings and Patent Postcard	
<input type="checkbox"/> Assignment Enclosed	<input checked="" type="checkbox"/> Cert. of Timely Mailing
	<input type="checkbox"/> Exp. Mail:

ORIGINAL



IDENTIFICATION OF APPLICATION

Serial No.:	09/676,197
Title:	METHOD OF FORMING A GATE ELECTRODE ON A SEMICONDUCTOR DEVICE AND A DEVICE INCORPORATING SAME
Applicant:	William R. Roche
Client:	Advanced Micro Devices, Inc.
Mailed:	01/17/03
Filed:	09/28/2000
Attorney:	JMA/MWS
File No.:	2000.032200/TT3634
Date:	03/03/2003



CERTIFICATE OF MAILING
37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:

01/17/03

Date

Kathy Hano

Signature

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Roche et al.

Batch No.: Unknown

Serial No.: 09/676,197

Filed: 9/28/2000

For: METHOD OF FORMING A GATE
ELECTRODE ON A SEMICONDUCTOR
DEVICE AND A DEVICE
INCORPORATING SAME

Group Art Unit: 2822

Examiner: DUONG, Khanh B.

Atty. Dkt. No.: 2000.032200/TT3634

RECEIVED
MAY 30 2003
TECHNOLOGY CENTER 2800

**SUBMISSION OF FORMAL DRAWINGS
PURSUANT TO NOTICE OF ALLOWABILITY MAILED DECEMBER 3, 2002**

ATTN OFFICIAL DRAFTSMAN
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicants hereby submit the formal drawings for the above-referenced application
Figure 6 and request that this corrected drawing be accepted for filing.



23720

PATENT TRADEMARK OFFICE

WILLIAMS, MORGAN & AMERSON
10333 Richmond Dr., Suite 1100
Houston, Texas 77042
(713) 934-7000

Date: January 17, 2003

Respectfully submitted,

Mark W. Sincell

Reg. No. 52226

Agent for Applicant

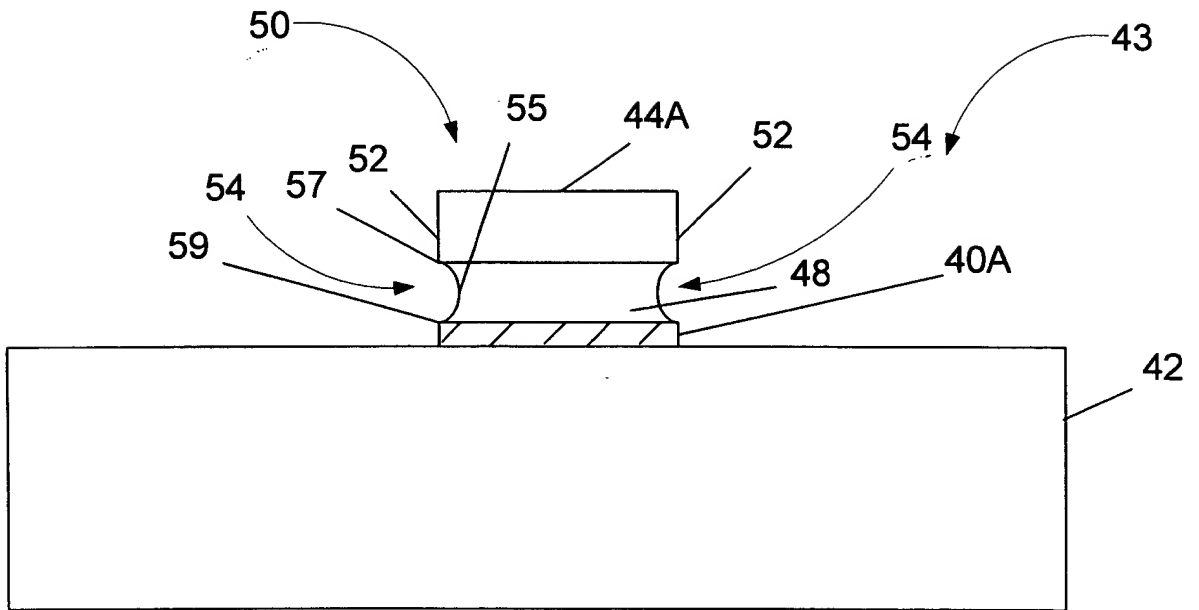


Figure 6